

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 10/431,822
Priority Filing Date May 8, 2003
 Inventor Terry Gilton et al.
 Assignee Micron Technology, Inc.
Priority Group Art Unit 2812
Priority Examiner Walter L. Lindsay Jr.
 Attorney Docket No. MI22-2462
 Customer No. 021567
 Title Methods of Forming Electronic Components, and a Conductive Line

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation application of co-pending application Serial No. 10/431,822, filed May 8, 2003. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 12-15-03

By: 

Mark S. Matkin
 Reg. No. 32,268

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2462		PRIORITY SERIAL NO. 10/431,822	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT: Terry Gilton et al.			
				PRIORITY FILING DATE March 8, 2003		PRIORITY GROUP ART UNIT 2812	

U.S. PATENT DOCUMENTS							
*Examiner's Initials	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
AA	4,599,118	07/1986	Han et al.				
AB	4,786,609	11/1988	Chen				
AC	4,954,867	09/1990	Hosaka				
AD	4,971,655	11/1990	Stefano				
AE	4,981,810	01/1991	Fazan et al.				
AF	5,015,598	05/1991	Verhaar				
AG	5,126,283	06/1992	Pintchovski				
AH	5,219,777	06/1993	Kang				
AI	5,252,352	11/1993	Woo et al.				

FOREIGN PATENT DOCUMENTS							
Document Number	Date	Country	Class	Subclass	Translation		
					Yes	No	
AJ	58-25265	05/1983	Japan (Imai)				
AK	62-90974	09/1987	Japan (Kawamura)				
AL	64-73772	03/1989	Japan (Koyama)				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)		
AM		Wolf et al., SILICON PROCESSING FOR THE VLSI ERA, pp. 66-71 (1986).
AN		Wolf et al., SILICON PROCESSING FOR THE VLSI ERA, pp. 208 and 212 (1986).
AO		Wolf et al., SILICON PROCESSING FOR THE VLSI ERA, pp. 216-218 (1986).
EXAMINER		DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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*Examiner's Initials	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
AA	5,286,344	02/1994	Blalock				
AB	5,290,720	03/1994	Chen				
AC	5,306,655	04/1994	Kurimoto				
AD	5,306,951	04/1994	Lee et al.				
AE	5,314,834	05/1994	Mazure et al.				
AF	5,322,807	06/1994	Chen et al.				
AG	5,334,556	08/1994	Guldi				
AH	5,371,026	12/1994	Hayden				
AI	5,382,533	01/1995	Ahmad et al.				

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	AA	5,420,800	05/1995	Fakui			
	AB	5,430,313	07/1995	Kumagai et al.			
	AC	5,439,846	08/1995	Nguyen et al.			
	AD	5,476,802	12/1995	Yamazaki			
	AE	5,491,100	02/1996	Lee et al.			
	AF	5,512,771	04/1996	Hiroki et al.			
	AG	5,545,578	08/1996	Park et al.			
	AH	5,552,329	09/1996	Kim et al.			
	AI	5,552,332	09/1996	Tseng			

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	AA	5,637,514	06/1997	Jeng			
	AB	5,668,028	09/1997	Bryant			
	AC	5,682,055	10/1997	Huang et al.			
	AD	5,714,413	02/1998	Brigham et al.			
	AE	5,739,066	04/1998	Pan			
	AF	5,759,901	06/1998	Loh et al.			
	AG	5,798,279	08/1998	Crisenze et al.			
	AH	5,897,353	04/1999	Kim et al.			
	AI	5,994,192	11/1999	Chen			

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AA	6,037,228	02/2000	Hsu				
AB	6,040,241	03/2000	Lee et al.				
AC							
AD							
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AF							
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